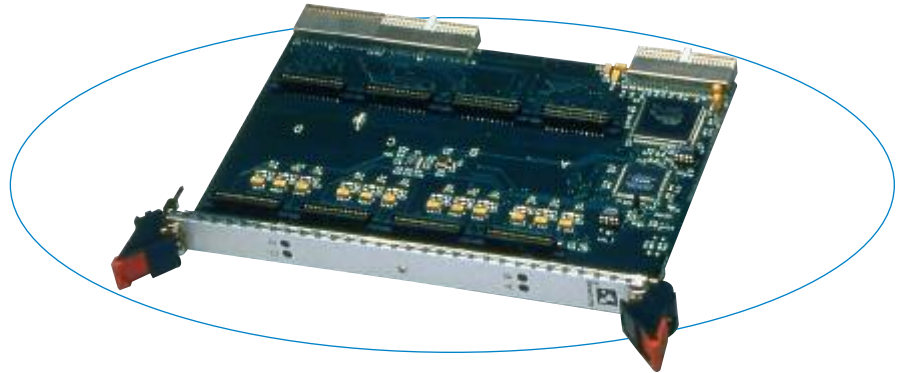


## AcPC8625 CompactPCI®, Non-intelligent, IP Carrier Card



The AcPC8625 is a non-intelligent slave board that interfaces four IP modules to the CompactPCI (cPCI) bus. All 200 I/O points are brought out the rear J4 and J5 connectors. This convenience eliminates messy cables from hanging out the front of the cage. In addition to a more efficient cage wiring design, it is also much easier to insert and replace boards. And with Acromag's 80mm transition module (TRANS-C200), all 200 I/O points are easily ported out the back of the cage.

### Features

- Four industry-standard IP module slots
- Board resides in memory space and
- Supports IP module I/O, ID and INT spaces
- 200 I/O points with rear access
- High-density rear connectors
- Compatible with all CompactPCI CPUs
- Plug-and-play carrier configuration and interrupt support
- Two interrupts per IP module
- Front panel LEDs
- Supervisory circuit for reset generation
- Individually filtered and fused power to each IP
- Ruggedized with ESD strip and EMC front panel
- Easily integrate IPs with your software using RTOS VxWorks, QNX, Linux, or Win DLL for Windows® 95/98/2000/XP.

### Benefits

- Clean system cabling.
- Easy board replacement.
- Simplified debugging with status LEDs.

Mix and match plug-in modules with different I/O functions to quickly create custom I/O boards with hundreds of channels.

### Operation

Acromag's carrier boards provide full data access to the IP module's I/O, ID and interrupt spaces. With full access to the programmable registers, you can easily configure and control the operation of the IP modules from the CompactPCI bus.

Up to two interrupt requests are supported for each IP module. All board interrupts are mapped to PCI bus INTA# signal.

Individual passive filters on each IP power supply line provide optimum filtering and noise isolation between the IP modules and the carrier board.

### Specifications

#### IP Compliance (ANSI/VITA 4)

Meets IP specs per ANSI/VITA 4-1995 (8MHz operation only) and IP I/O mapping to PICMG 2.4 R1.0.

Electrical/mechanical interface:

Supports single or double size IP modules.  
32-bit IP modules are not supported.

IP Module I/O space, ID space, and INT space supported.

Memory space: Not supported.

Interrupts: Supports two interrupt requests per IP module and interrupt acknowledge cycles via access to IP INT space.

#### CompactPCI bus Compliance

Meets PCI spec. V2.1 and PICMG 2.0, R2.1.

Data transfer bus: Slave with 32-bit, 16-bit, and 8-bit data transfer operation 32-bit read/write accesses are implemented as two 16-bit transfers to the IPs.

Interrupts: CompactPCI bus INTA# interrupt signal. Up to two requests sourced from each IP mapped to INTA#. Interrupt vectors come from IP modules via access to IP module INT space.

Plug-and-Play: The system maps the base address into the PCI bus 32-bit memory space.

### Environmental

Operating temperature: -25 to 85°C (AcPC8625)  
or -40 to 85°C (AcPC8625E models).

Storage temperature: -25 to 85°C (AcPC8625)  
or -40 to 85°C (AcPC8625E models).

Relative humidity: 5 to 95% non-condensing.

Power:

+5V (±5%): 250mA maximum.

±12V (±5%): 0mA (not used).

Plus IP module load.

MTBF: 409,808 hrs. at 25°C, MIL-HDBK-217F, notice 2.

### Ordering Information

#### Industry Pack Carriers

**AcPC8625:** CompactPCI carrier. Holds four IP modules.

**AcPC8625E:** Same as AcPC8625 plus extended temp. range.

**Software** (see [software documentation](#) for details)

**IPSW-API-VXW:** VxWorks® software support package

**IPSW-API-QNX:** QNX™ software support package

**IPSW-API-WIN:** Windows® DLL driver software support pkg.

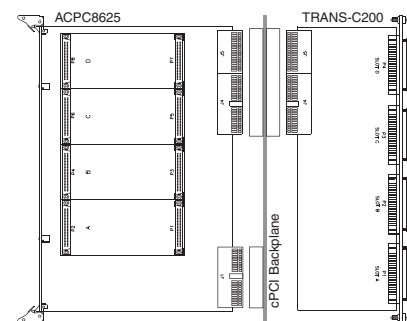
**IPSW-LINUX:** Linux™ support (website download only)

**Accessories** (see [accessories documentation](#) for details)

**5028-438:** Cable, SCSI-2 to SCSI-2, shielded.

**5028-378:** Termination panel, SCSI-2 connector,  
50 screw terminals

**TRANS-C200:** Transition module



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